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the display object 28. It should be understood that the outputs of the image edge detection circuit 26 and reference frame capture circuit 27 are both independent and complementary such that the reference frame capture circuit 27 may be used alone or in combination with the image edge detection circuit 26 or other methods to improve the quality of the digitally converted image displayed by the display object 28.

Please replace the paragraph on page 15, line 20 to page 16, line 11 with:

a2

Referring now to **FIG. 8**, wherein a block diagram overview of the functional components of the reference frame capture circuit 27 in accordance with one embodiment of the present invention is illustrated. As shown, the reference frame capture circuit 27 has a microcontroller interface connected to the microcontroller bus 42, and receives inputs from the line advance signal conductor 53, pixel clock signal conductor 54, and the  $V_{sync}$  frame advance signal conductor 58. The reference frame capture circuit 27 also receives pixel data input 101 from the color data signal channels 56a, 56b, 56c, through the output 101 of the pixel value calculator 97. Access to a current reference frame in stored frame memory is provided through an input port of the WRAM 132. A horizontal position counter (HPC) component 105 uses input from the line advance conductor 53 and the pixel clock conductor 54 to determine the current pixel position of the pixel data input 101. The reference frame capture circuit 27 receives a threshold value input 110 used by a pixel value comparator component 115 to compare the pixel values of the pixel data input 101 to the corresponding pixel values of the lines of the current reference frame. The reference frame capture circuit 27 further includes a reference frame capture switch 120 to trigger the microcontroller 16 to capture a new reference frame to store in WRAM 132 stored frame memory for eventual transmission to display object 28.

### IN THE CLAIMS

Please cancel claims 1-16 without prejudice, and add the following new claims:

a3  
can't  
sub  
C1

17. (New) A method comprising:
- converting a frame of analog image data to a frame of digital image data that includes pixel data;
- capturing the frame of digital image data;